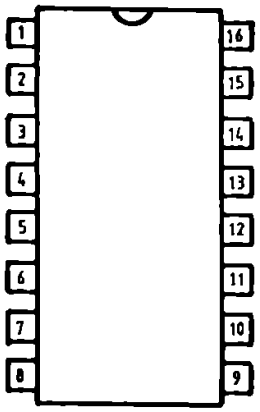
βM 3189
FM-IF HIGH FIDELITY SYSTEM

The βM 3189 is a monolithic integrated circuit that provides all the functions of a comprehensive FM-IF system. The block diagram of this circuit includes a three stage FM-IF amplifier-limiter configuration with level detectors for each stage, a double balanced quadrature FM detector and an audio amplifier that features the optional use of a muting (squelch) circuit. The advanced circuit design of the IF system includes desirable features such as programmable delayed AGC for the RF tuner, an AFC drive circuit and an output signal to drive a tuning meter and/or provide stereo switching logic. This IC is ideal for HI-FI operation.

Features

- Operating temperature	-25 ...	+70	oC
- Storage temperature	-25 ...	+125	oC
- Supply voltage	9 ...	16	V
- Supply current	20 ...	44	mA
- Input limiting voltage (-3 dB point)	max.	25	uV
- Recovered AF voltage (rms)	325 ...	625	mV
- Single tuned total harmonic distorsions	max.	1	%
- Double tuned total harmonic distorsions	typ.	0.1	%
- Signal plus noise-to-noise ratio	min.	65	dB
- AM rejection	min.	45	dB

1. IF input
2. Decoupling
3. Decoupling
4. RF GND
5. MUTING input
6. AUDIO output
7. AFC output
8. IF output
9. Detector input
10. Reference voltage
11. V+
12. MUTING output
13. Field indicator
14. GND
15. AGC output
16. AGC input



PACKAGE MP-117 / TOP VIEW