

**LINEAR
INTEGRATED
CIRCUITS
= VOLTAGE
= COMPARATORS =**



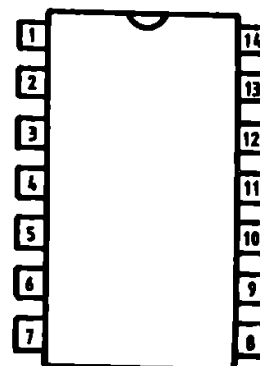
**CLB 2711EC
DUAL COMPARATOR**

The integrated circuit CLB 2711EC contains two voltage comparators with separate differential inputs, a common output and provision for strobing each side independently. With the addition of an external resistor network, can be used as a sense amplifier for core memories. The CLB 2711EC is also useful as a double ended limit detector, providing high, speed with lower power dissipation than two single comparators.

Features

- Operating temperature	0 ... +70 oC
- Storage temperature	-55 +125 oC
- Positive supply voltage	max. +14 V
- Negative supply voltage	min. -7 V
- Peak output current	max. 25 mA
- Differential input voltage	max. +/- 5 V
- Input voltage	max. +/- 7 V
- Strobe voltage	0 ... 6 V
- Input offset voltage	max. 5 mV
- Input offset current	max. 15 uA
- Input bias current	max. 75 uA
- Voltage gain	min. 700 -
- Response time	typ. 40 ns
- Output sink current	min. 0.5 mA

1. NC
2. Input 1-
3. Input 1+
4. V-
5. Input 2+
6. Input 2-
7. NC
8. NC
9. Strobe 2
10. Output
11. V+
12. GND
13. Strobe 1
14. NC



PACKAGE TO-116 / TOP VIEW